Title of the Talk: n-p-n TFETs: geometry and interface traps

Abstract of the Talk: This talk aims to present a novel geometry of TFETs based on the popularly known n-p-n architecture of BJTs. The objectives of the talk are (a) to acquaint the audience with the geometrical advantages of the n-p-n TFET, (b) draw analogy in terms of definitions and effects of geometrical parameters with p-i-n TFETs, and (c) throw light on the trap sensitivity of the device.