Sl No.	Paper Title
**List of the accepted papers (as on 20-10-2022)**
1	Study of the influence of parmeters to explore the Properties of Graphene Nanoribbon Field Effect Transistors
2	Design and Performance Analysis of DC-DC Sepic Converter for Specific Input Range
3	Performance Comparison and Design of Passive Components for DC-DC Buck-Boost, Cuk and Sepic Converter
4	Performance of vertical Van der Waals JLFET in 8nm channel length
5	Trench Gate JAM Dielectric Modulated Nanowire FET (TG-JAM-DM-NWFET) Biosensor
6	Performance analysis of PLL based DSGCP (Double Stage Grid Connected Photovoltaic) system with non-linear load under normal and various grid fault conditions
7	Macaroni Channel-Nanowire-Field Effect Transistor (MC-NW-FET) For Gate Induced Drain Leakage (GIDL) Reduction Application
8	Design and simulation of all-inorganic lead-free perovskite solar cell with 7% conversion efficiency
9	Structural Asymmetry related Nonlinear Mobility of Electron in InxGa1-xAs/GaAs Quantum well FET
10	IoT based Diabetic Retinopathy Monitoring System
11	Performance Optimization of CdS/CdTe Thin Film Hetrojunction solar cell by SCAPS Simulation
12	Low Power CMOS Comparator with low offset voltage and good resolution for 10-bit SAR ADC
13	Two dimensional modeling of dual material double gate TFET in stacked hetero-dielectrics with split high-K materials
14	A Scheme for Torque Ripple Minimization in BLDC Drive Using Two-Inductor Boost Converter
15	Doping Induced Threshold Voltage and ION/IOFF Ratio Modulation in Surrounding Gate MOSFET for Analog Applications
16	Modeling of temperature dependent electrical characteristic of quantum dot Single Electron Transistor (SET)
17	Design and Implementation of Area Efficient Approximate MAC Unit for Deep Neural Network based Architectures and Applications
18	Investigation of the Efficiency of CsGeI3-based solar cell using SCAPS-1D modeling and simulation
19	Performance Evaluation of LiNbO3-based Negative Capacitance Field Effect Transistors (NCFETs)
20	Selective growth of ZnO nanowires by employing voltage-assisted hydrothermal growth technique
21	Multisubband Electron Mobility in Parabolic Coupled Double Quantum Well Structure

22	A Bio-Material Based Write-Once-Read-Many Times Memory Using Sodium Caseinate and Polyvinylpyrrolidone Blend
23	Effect of Nitride Stress on Linearity performance of AlGaN/GaN HEMT
24	Doping-Less Underlapped Dielectric Modulating Bio-Tunnel FET Accomplishing Repulsive Steric Hindrance
25	Numerical Analysis of Hybrid Carrier Selective Passivating Contacts for n-Silicon Solar Cells
26	Analysis of certain electrical properties in Silicon nanowire field-effect transistors with high-κ HfO2 as gate dielectrics
27	Medical Diagnosis and Identification of Covid -19 by Intelligent IoT System and Resnet 18 Bilinear Deep Greedy Network
28	IoT Based Intelligent System for Covid - 19 hotspot detection by CNN Crowd Density Algorithm
29	Design and implementation of RFID based tracking algorithm for tracing the chicken suspected to be infected by Avian Influenza
30	Design of CMUT cell for High-intensity Focused Ultrasound Applications
31	Comparative Analysis of Short Channel Effects in Dopingless Charge Plasma Based Nanowire FET
32	Improvement of Transport Mobility in Asymmetric V-shaped Double Quantum Well Structure
33	A Study on Absorptance Enhancement of Multilayer Graphene Metamaterial in Terahertz Regime
34	ZnO Homojunction Based BTX Vapor Sensor Device
35	Interpreting Sensing Behavior of MoS2 Nanoflower Based Liquid Phase BTX Sensor Employing DFT Study
36	Ab-initio Study Based Understanding of Pnictogen Trihydrides Adsorption on WS2 Monolayer
37	Analog and RF Performance Analysis of SiO2/HfO2 Dual Dielectric Gate All Around Vertically Stacked Nanosheet FET for 5nm Technology Node
38	FPGA for secured hardware & IP ownership
39	Propagation of Terahertz Signal through Tropical Thunderstorm
40	Impact of Glancing Angle Deposition Technique on the Performance of SnS Thin Film Solar Cell: SCAPS-1D simulation
41	Performance Analysis of RTL to GDS-II Flow in Opensource Tool Qflow and Commercial Tool Cadence Encounter for Synchronous FIFO
42	Design and Simulation of Ultra-Low-Power Parallel Summation Logarithmic Amplifier
43	A RELIABLE PROTECTION SCHEME FOR SMARTGRID BASED ON WAVELET TRANSFORM AND ENSEMBLE CLASSIFIER.
44	Performance Assessment of a Dielectrically Modulated SiGe-Pocket DG TFET-based Biosensor

45	Gene Co expression analysis for identifying some regulatory genes in human lung cancer
46	Performance Analysis of Adiabatic CMOS Interface for Low Power Applications
47	Diminished Short Channel Effects (SCEs) in Junction Less Double Gate (JL DG) MOSFET
48	CV Analysis and Linearity Performance of InGaN Notch Dielectric Modulated Dual Channel GaN MOSHEMT for Reliable Label-free Biosensing
49	Analytical Modeling of Asymmetric Gate Stack Junctionless Dual Material Surrounding Gate MOSFET for Enhanced Hot Carrier Reliability
50	A Passive Lossless Snubber For the Boost Power Converters
51	Parametric Analysis and Fault Modeling of Next Generation High Speed Interconnects
52	Optimized Test Pattern Generation for Single Missing Gate Fault Detection in Reversible Arithmetic Circuits
53	VLSI and AES based IoT security by Modified Random S- Box Generation
54	Sensitivity analysis of Dielectric Modulated Dual Gate Ferroelectric Junctionless Transistor based Biosensor
55	Impact of Gaussian Grain Boundary Trap States on the Performance of the LTPS TFTs
56	Dibit-based NAND gate using Reflective Semiconductor Optical Amplifier
57	Comparative performance analysis of FinFET, CNTFET and GNRFET for low power digital logic circuit applications
58	Novel Mode Changing Features in the Design of Quaternary Logic Gates using CNTFET
59	A Unified Approach for Realization of IIR Filters in Delta Domain
60	Simulation of GaN-Based Polarization Junction Super HFET for Power Electronics Application
61	Design and Analysis of ARCH-shaped Gate All Around (GAA) Hetero-junction Tunnel Field Effect Transistor (TFET) For IoT Applications
62	Electrical Noise Analysis of Gate-on-Source Tunnel Field Effect Transistor
63	Study of Plasma Distribution Function in a Paul Trap using Palette Mapped 3D Plots
64	Ab Initio Study on Stability, Electronic and Optical Properties of Monolayer $Mo_{1-x}W_xSe_2$ Alloys
65	Interlayer twist angle-dependent electronic structure and optical properties of InSe/WTe2 van der Waals heterostructure
66	Direct wafer-bonded two terminal GaAsP/Si dual junction solar cell with 19.80% efficiency

67	III-V HETEROSTUCTURE TRANSISTER WITH UNDERLAP: A COMPARITIVE STUDY AND PERFORMANCE INVESTIGATION
68	Analytical model of a Dielectric-Modulated, Triple Material, Stacked Surrounding Gate Junctionless MOSFET-based label-free biosensor
69	Performance Comparison between PI based Control and Model Predictive Control of Voltage Source Inverter under Load Variations
70	Unified Surface Potential Based Analytical Modeling for Symmetrical Double Gate AlGaN/AlN/GaN MOS-HEMT for Label Free Bio-Sensing
71	Thermal Effects on Performance Parameters of Gate Stack DG-MOSFET based Bio-Sensor
72	Smart IoT based Early Stage Drowsy Driver Detection Management System
73	Effect of deep and tail grain boundary trap states on the performance of poly-ZnO TFT
74	Reactive Oxygen Species (ROS) Sensing- A Nanoscale Transistorized Approach
75	Analytical modelling of Current Spreading Length in Flip chip GaN LEDs
76	Computation Study of WSe2 Monolayer for Biomarker in Lung Cancer
77	Analog/RF Performance Analysis of a Novel Si0.9Ge0.1/InAs Charge Plasma Based Junctionless TFET
78	High-k SOI GaN FinFET for high power and high frequency applications
79	Design and Simulation of Double gate Junctionless Field Effect Transistor for Ammonia Gas Sensing
80	Impact of Gate engineering on Analog, RF Performance of Nanoscale Barriered TM-Heterostructure DG-MOSFET.
81	2-BIT PASS-TRANSISTOR LOGIC POTENTIOMETRIC DAC USING .15um CMOS TECHNOLOGY
82	Performance analysis of New Dual Pocket Tunnel FET Based Biosensor
83	A Novel Technique of Enhancing CMRR of Fully-Differential Instrumentation Amplifier Using High Gain CMFB Loop
84	Performance analysis of CSVCO using CMOS and Beyond CMOS Technologies - A Review
85	Influence of total ionizing dose on LWM Bulk and SOI p-FinFET
86	Multi-objective Genetic Algorithm for Optimal Design of CMOS Ring Oscillator
87	Tunable frequency CMOS OTA-C sinusoidal oscillators for high frequency applications
88	The Effect of Back-Barrier on the Performance Enhancement of III-Nitride/β-Ga2O3 Nano-HEMT
89	Study on Sensitivity Parameters of Staggered Heterojunction Gate Stack Tunnel FET Biosensor

90	Reversible High Speed Binary Content Addressable Memory array design using Transmission Gate Logic
91	Analysis of IR drop and SSN in CNT and GNR Power Distribution Networks
92	Work-Function Variability impact on the performance of Vertically Stacked GAA FETs for sub-7nm Technology Node
93	Simulation Study of Charge Plasma Nanotube FET for Low Power Mixed Signal Circuit in IoT Applications
94	QCA based Majority Voter Design using IBMQ
95	Reversible Vigenere Cryptographic Cipher in Quantum-Dot Cellular Automata
96	A Novel Extended Back-Gate Negative Capacitance TFET for Improved Device Performance
97	Drain Engineered Charge Plasma-based Vertical TFET for Improved Device Performances
98	Extended-Source GaSb/GaAs Heterojunction GAA-TFET to Improve ION/IOFF Ratio
99	Design and Investigation of Mg2Si Source Charge Plasma Based DGTFET for Biomolecule Detection.
100	Complex Odia Handwritten Character Recogne of the ongoing uses of offline OCR is the recognition of photographs of Odia characters. Here, an effort has been madnition using Deep Learning Model
101	Design and Optimization of Highly Efficient a-Si:H/μc-Si:H Tandem Solar Cell
102	Modelling of Surface Potential and Threshold Voltage for Short Channel Junctionless Cylindrical Gate-All-Around MOSFET
103	Investigation of Novel Z-shaped Gate TFET with Improved Device Characteristics
104	A 0.3 V, 4th order low-pass OTA-C filter using bulk-driven technique for EEG applications
105	Design of a low-power 3rd order notch filter for biomedical applications
106	Implementation and Investigation of Quantum Characteristics of Cytosine Single –walled Nanotube Transistor: A DFT Based Approach
107	Design of a linearized operational transconductance amplifier using quasi-floating bulk resistor
108	A Hybrid Boost-Flyback Converter Suitable For Power Factor Correction
109	Computation of In-Plane and Characteristic Impedance of Microstrip Structure under Plasmonic Resonance
110	Radiation Tolerant by Design 12-Transistor Static Random Access Memory
111	Single Event Upset Mechanism in SRAM Latch and Its Circuit-Level prevention Technique

112	Analysis of AlGaN/GaN Based HEMT for Millimeter-Wave Applicatios
113	Design of a Passive Beamforming Circuit with -37.5 dB Return Loss and -42 dB Isolation Loss for a Multibeam Antenna in 5G Applications
114	Design of a Highly Directive, Wideband and Compact Endfire Antenna Array for 5G Applications
115	A super threshold compact silicon neuron circuit for different neural dynamics suitable for spiking neural network
116	An Ultra-Low Power (86 nW) Low-Voltage (0.6 V) Self-Biased Instrumentation Amplifier for Bio-Medical Applications
117	Review on Wide Band Gap Semiconductor
118	A Review Article on the usage of Different Materials and Different Device architectures in the Design of Tunnel Field Effect Transistor
119	Analytical Investigation of Gate-to-Drain Leakage Current for Junctionless Accumulation-Mode MOSFET
120	A Novel Architecture of a fault-tolerant Memory System based on Missing Data Imputation